

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	4181	(370/371,374,378,381,412,422).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/03/14 18:56
L2	4181	L1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/03/14 18:56
L3	49	2 and (count\$3 with buffer near5 (enqueu\$3 or allocat\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/03/14 18:57
L4	67	2 and (count\$3 with queue near5 (enqueu\$3 or allocat\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/03/14 19:34
L5	0	(ingress module and memory controller circuit and destination resolution circuit).clm.	US-PGPUB	ADJ	ON	2009/03/14 19:45
L6	0	(ingress module and memory controller circuit).clm.	US-PGPUB	ADJ	ON	2009/03/14 19:45
L7	39	(ingress module).clm.	US-PGPUB	ADJ	ON	2009/03/14 19:46
L8	0	(ingress module and forwarding module).clm.	US-PGPUB	ADJ	ON	2009/03/14 19:46
L9	31	(ingress module and egress module).clm.	US-PGPUB	ADJ	ON	2009/03/14 19:46
L10	0	(ingress module and egress module and forward module).clm.	US-PGPUB	ADJ	ON	2009/03/14 19:46
L11	0	(ingress module and egress module and forwarding module).clm.	US-PGPUB	ADJ	ON	2009/03/14 19:47

3/ 14/ 2009 7:47:54 PM

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